

GP2T040A120X

QSiC™ 1200V SiC MOSFET		V _{DS} R _{DS,on} I _{D (TC=250} T _j ,max	c) 63 A
Features	Chip Outline and I	nner Circuit	
 High speed switching Reliable body diode All parts tested to greater than 1,400V Benefits Lower capacitance Higher system efficiency Easy to parallel 		(1) GO (3) S	(1) G (Gate) (2) D (Drain) (3) S (Source
Applications Solar Inverters 	Part #	Top Side	Bottom Side
 Switch mode power supplies, UPS Induction heating and welding EV charging stations High voltage DC/DC converters Motor drives 	GP2T040A120X	AI	Ni/Ag

Maximum Ratings, at $T_j=25^{\circ}C$, unless otherwise specified

Characteristics	Symbol	Conditions	Values	Unit
Drain-Source Voltage	V _{rated}	V _{GS} =0V, I _{DS} =1µA	1200	V
Continuous Drain Current	I _D *	T _C =25 °C, T _j =175 °C	63	А
	D	T _C =100 °C, T _j =175 °C	47	
Pulsed Drain Current	I _{D,pulse} **	T _C =25°C	160	
Gate Source Voltage	V _{GSmax}		-10/25	V
	V _{GSop}	Recommended operational	-5/20	v
Operating & Storage Temperature	T _j , T _{storage}	Continuous	-55175	°C

Values have been verified on SemiQ TO-247-3L packaged devices

* Assumes R_{thJC} thermal resistance of 0.47°C/W with recommended wire bonds

** Pulse width is limited by $T_{jmax} \label{eq:transformation}$

Refer to the Warnings and Notes at the end of this document

GP2T040A120X

Static Electrical Characteristics, at T_i=25°C, unless otherwise specified

Characteristics	Symbol	Conditions	Values			Unit
Characteristics			min.	typ.	max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS} ***	I _{DS} =1mA	1200	-	-	V
Zara Cata Valtaga Drain Current	1	V _{DS} =1200V, V _{GS} =0V***	-	0.1	1.0	
Zero Gate Voltage Drain Current	DSS	V _{DS} =1200V, V _{GS} =0V, T _j =175°C	-	1	-	μΑ
Cata Sauraa Laakaga Current	000.	V _{GS} =20V, V _{DS} =0V	-	<+10	100	۳٨
Gate-Source Leakage Current	I _{GSS-} ***	V _{GS} =-5V, V _{DS} =0V	-	>-10	-100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{GS} =V _{DS} , I _{DS} =10mA***	1.8	2.4	4	V
		$V_{GS}=V_{DS}$, $I_{DS}=10$ mA, $T_j=125$ °C	-	1.8	-	
		V _{GS} =V _{DS} , I _{DS} =10mA, T _j =175°C	-	1.6	-	
Drain-Source On-Resistance		V _{GS} =20V, I _{DS} =40A***	-	37	52	
	R _{DSon}	V _{GS} =20V, I _{DS} =20A	-	35	45	- mΩ
		V _{GS} =20V, I _{DS} =40A, T _j =125°C	-	56	-	
		V _{GS} =20V, I _{DS} =40A, T _j =175°C	-	73	-	
Transconductance	9 _{fs}	V _{DS} =20V, I _{DS} =40A	-	16	-	S
Gate Input Resistance	R _G	f=1MHz, V _{AC} =25mV, D-S Short	-	1.9	-	Ω

AC Electrical Characteristics, at T_j =25°C, unless otherwise specified

Characteristics	Symbol Conditions	Conditions	Values			Unit
Characteristics		min.	typ.	max.	Unit	
Input Capacitance	CISS	×(−0)(-	3192	-	
Output Capacitance	C _{OSS}	V _{GS} =0V, V _{DS} =1000V, f=200kHz, V _{AC} =25mV	-	132	-	pF
Reverse Transfer Capacitance	C _{RSS}		-	7	-	
Coss Stored Energy	E _{oss}		-	77	-	μJ
Total Gate Charge	Q _G	V(-800)(L -20A	-	118	-	
Gate to Source Charge	Q _{GS}	V _{DD} =800V, I _{DS} =20A, V _{GS} =-5/+20V	-	53	-	nC
Gate to Drain Charge	Q _{GD}		-	23	-	

Body Diode Characteristics, at T_i=25°C, unless otherwise specified

Characteristics	Symbol	Conditions	Values			Unit
Characteristics	Symbol		min.	typ.	max.	Unit
Max Continuous Diode Fwd Current	l _s *	V _{GS} =-5V, T _C =25°C	-	-	74	A
Diode Forward Voltage	V _{SD} ***	V _{GS} =-5V, I _{SD} =20A	-	3.8	-	V
Reverse Recovery Time	t _{RR}	I _{SD} =20A, V _R =800V, V _{GS} =-5V,	-	24	-	ns
Reverse Recovery Charge	Q _{RR}	$di_F/dt=3.6A/ns$	-	297	-	nC
Peak Reverse Recovery Current	I _{RRM}		-	21	-	A

Values have been verified on SemiQ TO-247-3L packaged devices

For examples of switching characteristics, please refer to packaged datasheets GP2T040A120U and GP2T040A120H

* Assumes R_{thJC} thermal resistance of 0.47°C/W with recommended wire bonds

*** Verified by 100% wafer test

GP2T040A120X

Mechanical Parameters

Parameter	Тур.	Unit
Wafer Size	150	mm

Typical Performance

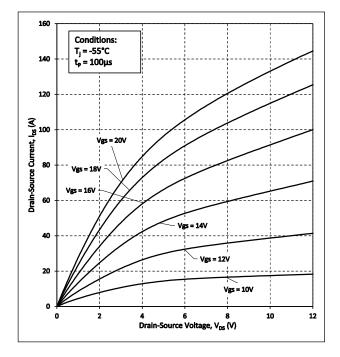


Figure 1. Output Characteristics $T_j = -55^{\circ}C$

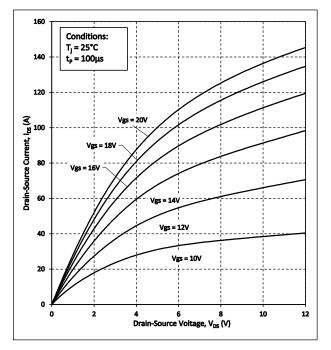


Figure 2. Output Characteristics T_j = 25°C

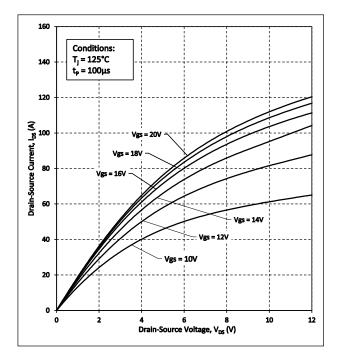


Figure 3. Output Characteristics T_i = 125°C

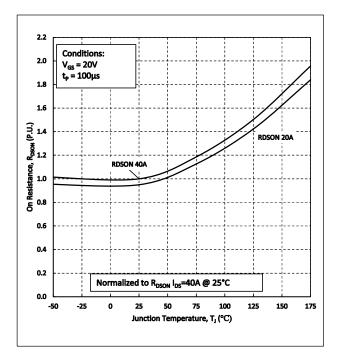


Figure 5. Normalized On-Resistance vs. Temperature

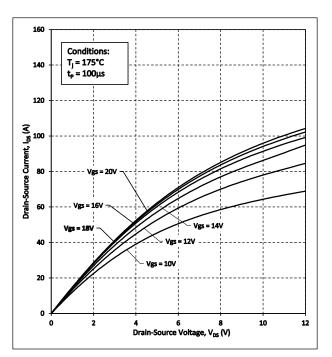


Figure 4. Output Characteristics T_i = 175°C

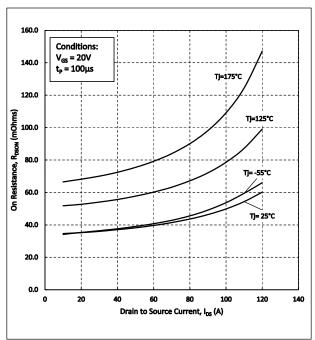


Figure 6. On-Resistance vs. Drain Current For Various Temperature

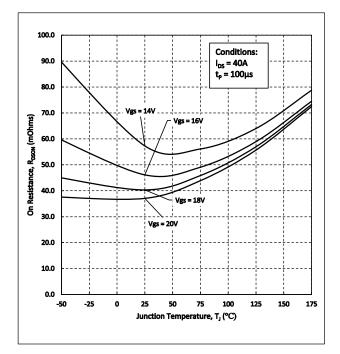


Figure 7. On-Resistance vs. Temperature For Various Gate Voltages

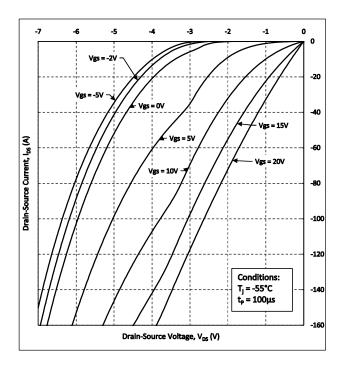


Figure 9. Body Diode Characteristics at T_i = -55°C

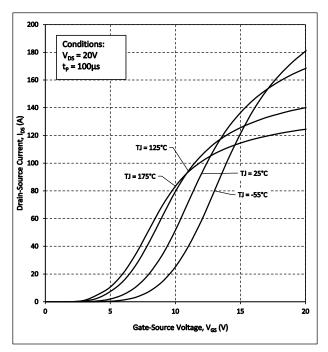


Figure 8. Transfer Characteristic for Various Junction Temperatures

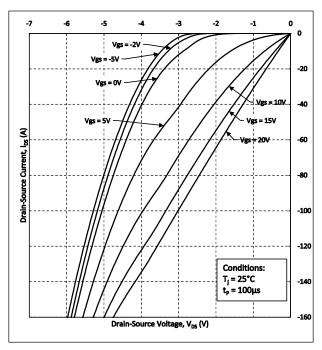


Figure 10. Body Diode Characteristics at T_i = 25°C

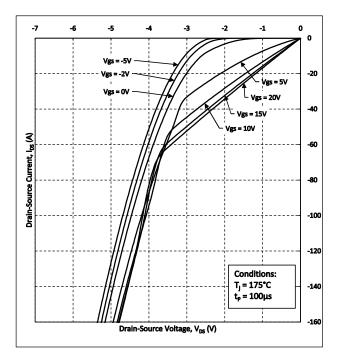


Figure 11. Body Diode Characteristics at T_i = 175°C

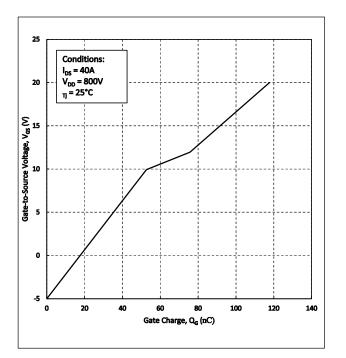


Figure 13. Gate Charge Characteristics

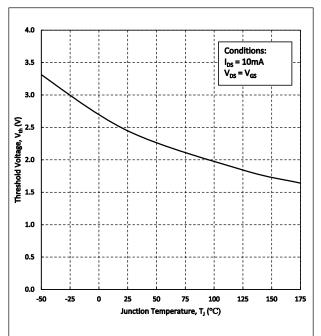


Figure 12. Threshold Voltage vs. Temperature

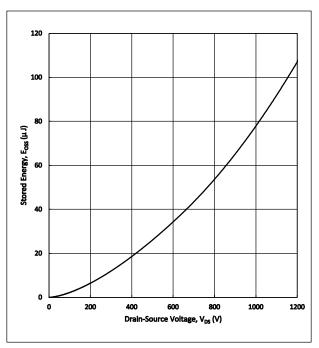


Figure 14. Output Capacitor Stored Energy

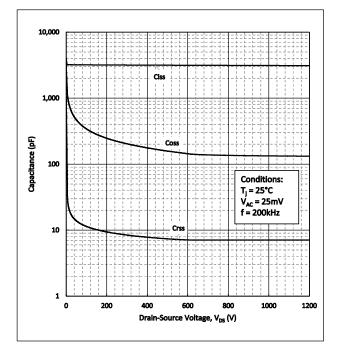
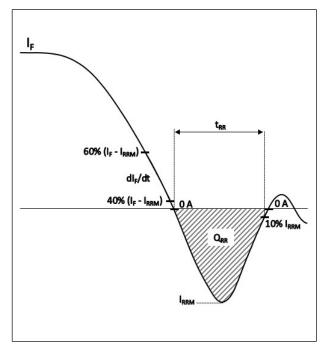


Figure 15. Capacitance vs Drain-Source Voltage





Revision History

Date	Revision	Notes
8/12/2022	1.0	Initial release of datasheet
10/24/2022	1.1	Updated Qgd, schematic
3/3/2023	1.2	Updated Vth_min and warnings
2/16/2024	1.3	Reduced mechanical information

Warnings

Except as other wise explicitly approved by SemiQ in a written document signed by authorized representatives of SemiQ, SemiQ's products may not be used in any applications where a failure of the product or any consequences of the use thereof can reasonably be expected to result in personal injury.

Notes

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REACh Compliance

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